

ZHKD UMA(11.6")

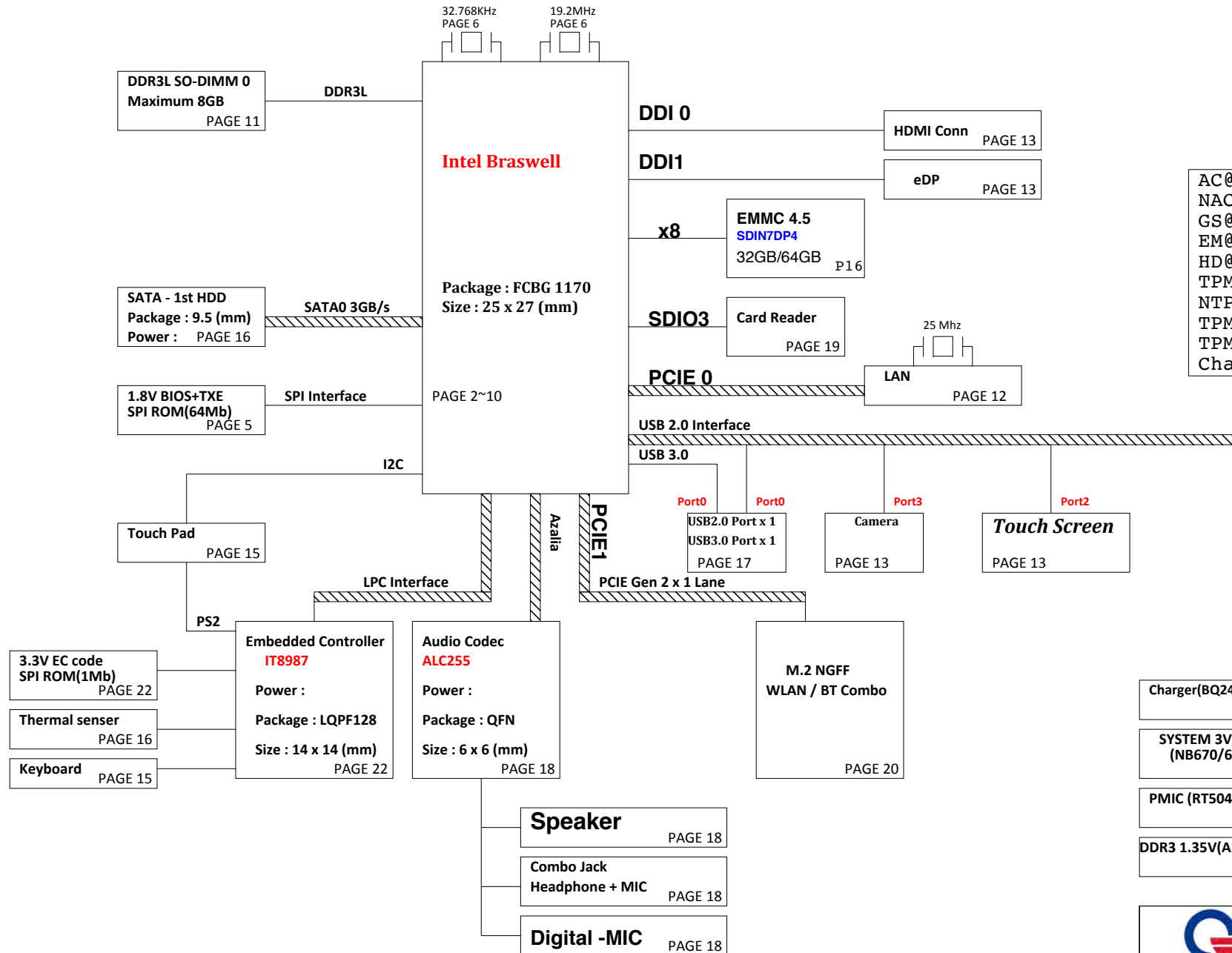
Intel Braswell Platform Block Diagram

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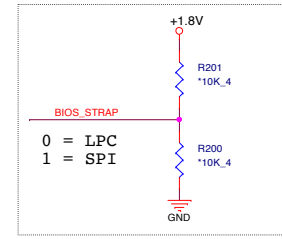
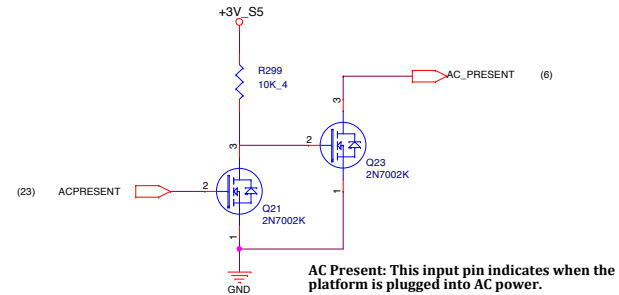
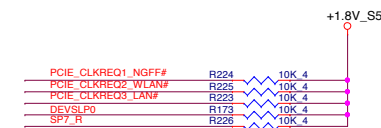
PCB 6L STACK UP

LAYER 1 : TOP
LAYER 2 : SGND
LAYER 3 : IN1(High)
LAYER 4 : IN2
LAYER 5 : SVCC
LAYER 6 : BOT

AC@ : IOAC
NAC@ : Non-IOAC
GS@ : G sensor
EM@ : eMMC
HD@ : HDD
TPM@ : TPM
NTPM@ : Non-TPM
TPM_I@ : 英飛凌
TPM_N@ : 新唐
Change CPU & EMMC P/N



Charger(BQ24737RGR)	VCC0+1 (RT8171BGQW)
PAGE 23	PAGE 27
SYSTEM 3V/5V (NB670/671)	+VGG (RT8171BGQW)
PAGE 24	PAGE 28
PMIC (RT5041AGQW)	Thermal / Discharge
PAGE 25	PAGE 29
DDR3 1.35V(APW8819)	
PAGE 26	



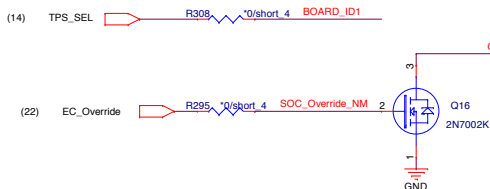
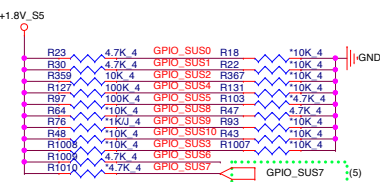


NGFF

WLAN

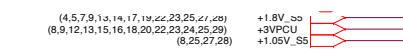
LAN

Hardware Strap	Strap Description
GPIO_SUS[0]	0 = DD10 not detected 1 = DD10 detected
GPIO_SUS[1]	0 = DD11 not detected 1 = DD11 detected
GPIO_SUS[2]	Top Swap(A16 Override) 0 = Change Boot Loader address 1 = Normal Operation
GPIO_SUS[4]	BIOS Boot Selection 0 = No SPI 1 = SPI
GPIO_SUS[5]	Security Flash Descriptors 0 = Override 1 = Normal Operation
GPIO_SUS[8]	ICLK_USB 2.0,DD1 SFR supply select 0 = Supply is 1.25V 1 = Supply is 1.05V
GPIO_SUS[9]	ICLK_USB 2.0,DD1 SFR Bypass 0 = No bypass 1 = Bypass with 1.05V (internal PU)
GPIO_SUS[10]	PMON Select 0 = Fuse controller 1 = PMC (internal PU)

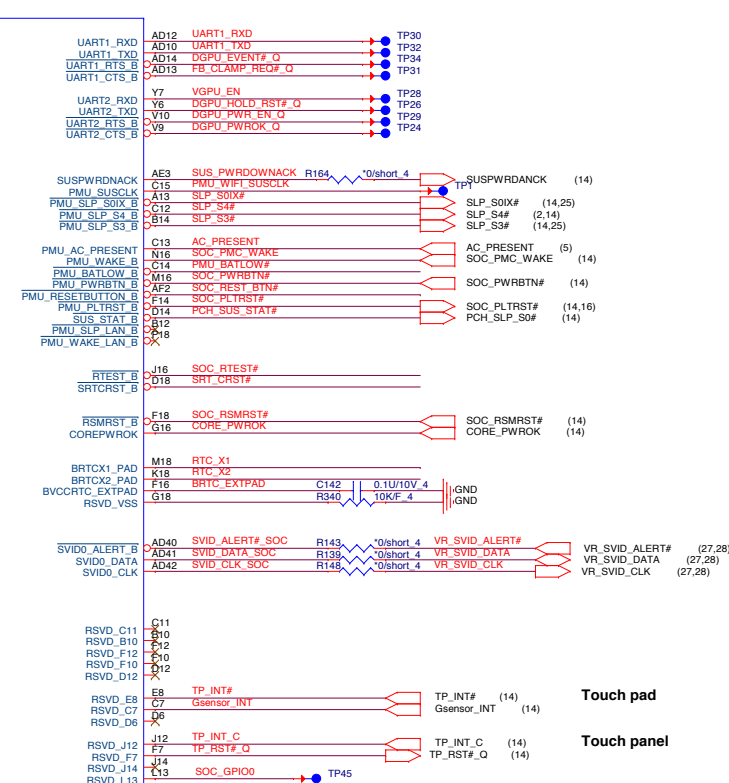


BOARD ID

BOARD_ID0 : "H" W/ G-Sensor; "Low" W/O G-Sensor
 BOARD_ID1 : "H" W/ touch panel; "Low" W/O touch panel
 BOARD_ID2 : "H" is UMA; "Low" is Dis
 BOARD_ID3 : TPM; HIGH=TPM; LOW=W/O TPM
 BOARD_ID4 : "Low" is for ZHKD,MB ID
 BOARD_ID5 : IOAC; High=IOAC,Low=non-IOAC



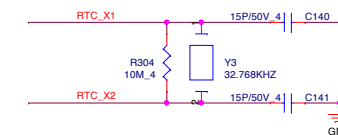
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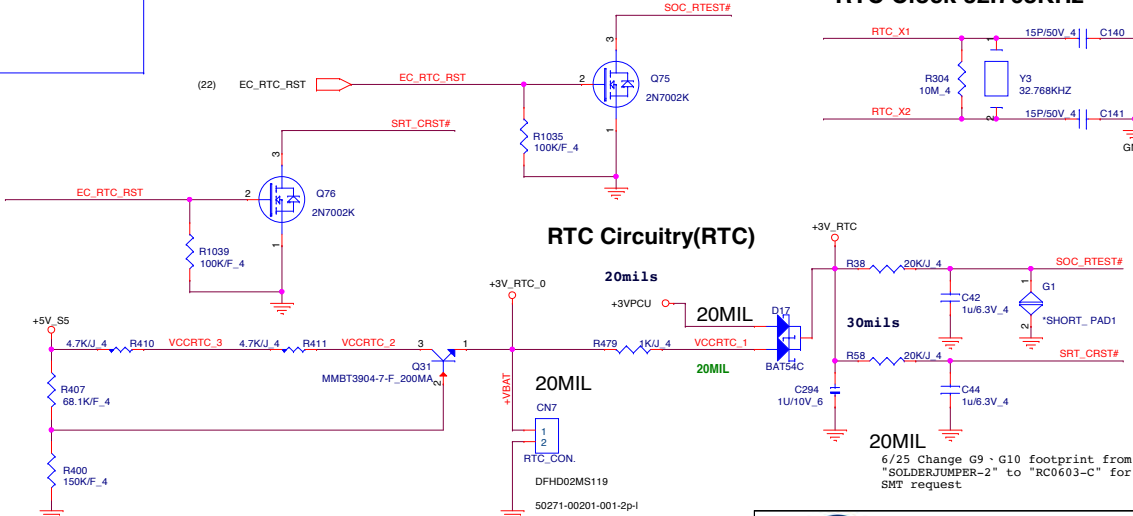
Touch pad

Touch panel

RTC Clock 32.768KHz



RTC Circuitry(RTC)



EC(KBC)

